



CHANDIGARH COLLEGE OF ENGINEERING & TECHNOLOGY

Sector-26, Chandigarh – 160019 (U.T.), INDIA

Approved by AICTE, New Delhi/Affiliated to Panjab University, Chandigarh
(Government Institute under Chandigarh(UT) Administration)



NOTICE

The Ph.D Viva-Voce Examination of Mr. Sudhakar Kumar on his Ph.D thesis titled, **“DESIGN OF AN ALGORITHM TO MINIMIZE SPECULATIVE PARALLELISM OVERHEAD IN SIMULTANEOUS MULTITHREADING ON MULTICORE CHIP ARCHITECTURE”** is scheduled to be held on 24th January, 2024 at 10:00 A.M. in Computer Lab-4, Room No. 215 of Chandigarh College of Engineering and Technology, Sector-26, Chandigarh.

All the faculty Members/Research Scholars and other interested may attend the same.

Principal
cum-Chairperson,(PURC)
Chd. College of Engg. & Tech
Sector-26, Chandigarh

Endst No. CCET/DG/Acds/RF-157/2024/5838-48

Dated:- 19/01/24

A copy is forwarded to the following for information and necessary action:-

1. The Asstt. Registrar (Secrecy) Panjab University, Chandigarh with reference to Memo No. 28736/ Secy/Theses, dated 07.12.2023.
2. Prof. Awadhesh Kumar Singh, (External examiner), NIT Kurukshetra (Haryana)
3. Prof. Sunil K Singh, Supervisor, CCET, Chandigarh
4. Prof. Naveen Aggarwal, Supervisor, UIET, Sector-25, Chandigarh
5. All the HODs, CCET, Chandigarh
6. Prof. I/C PURC, CCET Chandigarh
7. Incharge Website, CCET for uploading the Notice on College Website.
8. HOD (CSE), CCET is requested to kindly make the lab available on the above mentioned date and time
9. Mr. Sudhakar Kumar, Ph.D Scholar, CCET Chandigarh
10. PA to Principal, CCET Chandigarh
11. Notice Board

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cum-Chairperson,(PURC)
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